

OPEN POSITIONS FOR 2021

ANALOG DESIGN ENGINEER

At Melexis, you will design analog blocks from specification to implementation. You will take ownership of your own designed blocks and work together with Layout Engineers during topology definition. You will support Product Verification engineers during characterization phase (lab evaluation, debugging and validation of the Integrated Circuits).

MIXED SIGNAL DESIGN ENGINEER

At Melexis, you will be responsible for the definition, integration, simulation and debugging of the three main domains in our ICs - analog, digital and software. You will also support the Product Verification and Test departments in characterizing the final product and implementing an efficient test program.

LAYOUT IC DESIGN ENGINEER

At Melexis, you will translate the block and top level schematics to layout topology taking into account technology rules and specific requirements from Analog, Digital and Package departments. Your responsibilities include running of Layout vs. Schematic, Design Rules and other specific layout checks. You will also take part of the IC definition phase by creating Chip Floorplan (pinout and blocks positions).

TEST ENGINEER

At Melexis you will develop and validate the solution in order to test the Integrated Circuit (IC). Your test solution will be implemented to test 100% of the chip at 3 different temperatures before it is delivered to the customer. More specifically, you will take ownership of a complete test solution development, ensure the test solution robustness for sustainable manufacturing, create and validate the hardware (PCB) and software (test program) needed for the test solution, work together with the design team to ensure the optimal test coverage and use statistical methods during the evaluation of measurement results.

PHYSICAL DESIGN ENGINEER

At Melexis, you will be part of our global Digital Design team which is responsible for the IC's back-end. You will learn how to constrain a circuit in terms of timing, topology, power and test, get familiar with design for test techniques such as Full-scan and MBIST. Then you will exercise synthesis, placement, clock trees and routing phases. Lastly you will get familiar with equivalence checking, static timing analysis, LVS and DRC.

CAD ENGINEER

At Melexis, you will be part of the Global IT and will assure the installation and support on all used EDA/non-EDA tools, foundry development kits and associated technology files in order to provide all R&D teams overall development sites with a well-performing, stable, standard and intuitively usable CAD environment.